



LAKIREDDY BALI REDDY COLLEGE OF ENGINEERING (AUTONOMOUS)

L.B.REDDY NAGAR, MYLAVARAM-521230 :: NTR DT.:: A.P.:: INDIA

Results Sheet

Programme : M.Tech. (I Semester) (R23) Regular  
Specialization : Data Science

Exam Month & Year : Feb-2025

Course code & Name

08) 23DS62-Python for Data Science Lab

- 01) 23DS01-Statistical Foundations for Data Science
- 02) 23DS02-Machine Learning
- 03) DS-PE1- (P.E-I) Cloud Computing and Virtualization(23DS03)
- 04) DS-PE2- (P.E-II) Social Media Analytics(23DS07)
- 05) 23RM01-Research Methodology and IPR
- 06) 23AC05-Constitution of India
- 07) 23DS61-Machine Learning Lab

S.No.	H.T.No.	23DS01	23DS02	DS-PE1 23DS03	DS-PE2 23DS07	23RM01	23AC05	23DS61	23DS62	SGPA
1	24761D8801	C	B	B 23DS03	B 23DS07	B	Y	S	S	8.28
2	24761D8802	C	A	A 23DS03	A 23DS07	A	Y	S	S	8.89
3	24761D8803	C	B	B 23DS03	B 23DS07	B	Y	S	S	8.28
4	24761D8804	C	B	A 23DS03	B 23DS07	A	Y	S	S	8.56
5	24761D8805	-Ab-	-Ab-	-Ab- 23DS03	-Ab- 23DS07	-Ab-	-Ab-	-Ab-	-Ab-	0.00
6	24761D8806	B	A	B 23DS03	B 23DS07	A	Y	S	S	8.72

07 MAR 2025

Date :

Verified by

1 : K. Srinivas  
2 : *[Signature]*

CONTROLLER OF EXAMINATIONS

*[Signature]*  
PRINCIPAL

Note : Any discrepancy noted should be brought to the notice of C.o.E. immediately.



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Results Sheet

Programme : M.Tech. (I Semester) (R23) Regular  
Specialization : Power Electronics and Electrical Drives

Exam Month & Year : Feb-2025

Course code & Name

08) 23PE62-Simulation of Power Converters & Drives-I Lab

- 01) 23PE01-Analysis of Power Converters
- 02) 23PE02-Control of Motor Drives-I
- 03) PE-PE1- (P.E-I) Power Quality(23PE03)
- 04) PE-PE2- (P.E-II) Meta Heuristics Optimization Techniques(23PE08)
- 05) 23RM01-Research Methodology and IPR
- 06) 23AC05-Constitution of India
- 07) 23PE61-Power Converters & Drives-I Lab

S.No.	H.T.No.	23PE01	23PE02	PE-PE1 23PE03	PE-PE2 23PE08	23RM01	23AC05	23PE61	23PE62	SGPA
1	24761DAN01	B	A	A 23PE03	S 23PE08	B	Y	A	S	9.00
2	24761DAN02	B	B	B 23PE03	A 23PE08	B	Y	A	S	8.50
3	24761DAN03	F	C	B 23PE03	B 23PE08	B	Y	A	A	6.72

07 MAR 2025

Date : Verified by 1: *K. Sully* CONTROLLER OF EXAMINATIONS

2: *[Signature]*

*[Signature]*  
PRINCIPAL

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**Results Sheet**

**Programme** : M.Tech. (I Semester) (R23) Regular  
**Specialization** : VLSI and Embedded Systems Design

**Exam Month & Year** : Feb-2025

**Course code & Name**

08) 23VE62-Embedded System Design Lab

- 01) 23VE01-Digital VLSI System Design
- 02) 23VE02-Embedded System Design
- 03) VE-PE1- (P.E-I) Cryptography and Network Security(23VE03)
- 04) VE-PE2- (P.E-II) Image and Video Processing(23VE06)
- 05) 23RM01-Research Methodology and IPR
- 06) 23AC01-English for Research Paper Writing
- 07) 23VE61-Digital VLSI System Design Lab

S.No.	H.T.No.	23VE01	23VE02	VE-PE1 23VE03	VE-PE2 23VE06	23RM01	23AC01	23VE61	23VE62	SGPA
1	24761DAP01	F	F	C	B	B	Y	A	B	5.28
2	24761DAP02	A	C	C	A	B	Y	A	B	8.11

07 MAR 2025

Date : Verified by 1: *K. Sillu* CONTROLLER OF EXAMINATIONS

2: *[Signature]*

*[Signature]* PRINCIPAL

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